

Docket No.: 0885-CS

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: **RESPONSE UNDER 37 CFR 1.116**

Joel PAGE, et al.

: **EXPEDITED PROCEDURE**

Serial No.: 09/153,864

: Group Art Unit: 2816

Filed: September 16, 1998

: Examiner: J. Zweizig

For: POWER ON RESET TECHNIQUES FOR AN INTEGRATED CIRCUIT CHIP

RESPONSE UNDER 37 CFR 1.116

Box AF

Commissioner for Patents

Washington, DC 20231

Sir:

This is in response to the Examiner's Final Rejection of September 29, 2000. A Notice of Appeal is being filed simultaneously with this response.

Since the requirement to correct drawings is so intimately related with the rejection on the merits under 35 USC 112, first paragraph, Applicants respectfully request that the drawing requirement be held in abeyance pending resolution of the appeal of the rejection on the merits.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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